

Amended claims

1. A card memory device (20) having an input/output device (1) and having embedded:
a microprocessor (2);
5 a plurality of memory units (5-12, 16, 17) for storage of digital data, and
a selecting device (3, 4) for selecting one of the memory units (5-12, 16, 17) and for
routing the address information and data to and/or from the selected memory unit (5-12,
16, 17), characterized in that:
the microprocessor (2) is interposed between the input/output device (1) and the
10 selecting device (3, 4) and the microprocessor is adapted for:
 routing through all data and address information sent to and sent by the card
 memory device (20), and
 for supplying address information which relates to the data sent to each selected
 memory unit (5-12, 16, 17).
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2. The card memory device according to claim 1, wherein the addressing scheme for
each memory location of a memory unit includes a parallel and a serial portion.
3. The card memory device according to claim 2, wherein a parallel port of the selecting
20 device is connected to a parallel port of the microprocessor for receiving the parallel
portion of a card memory address.
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4. The card memory according to claim 2 or 3, wherein a first serial port of the selecting
device is connected to a serial port of the microprocessor for receiving the serial portion
25 of a card memory address.
5. The card memory device according to any of claims 2 to 4, wherein the selecting
device has a plurality of second serial ports, one connected to each memory unit via a
serial bus and the parallel address portion defines one of the second serial ports of the
30 selecting device.

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6. The card memory device according to any previous claim, wherein the memory capacity is at least 1 Mbytes.
7. The card memory device according to any previous claim, further comprising a first set of surface contacts including a ground contact, a power source contact and a data input and/or output contact, and the first set corresponding to a second set of contacts in a card memory device reader, the ground contact of the first set being arranged to ground any contact of the second set before this reader contact makes contact with any of the power and/or data contacts.
8. The card memory device according to claim 7, wherein the ground contact on the card memory device surrounds the data contact on three sides.
9. A method of using a card memory device having an input/output device and having embedded therein a microprocessor and a plurality of memory units for storage of digital data, the method comprising:
selecting one of the memory units; and
routing address information and data to and/or from the selected memory unit,
characterized by
routing through the microprocessor all data and address information set to and sent by the card memory device; and
supplying address information from the microprocessor relating to the data sent to each selected memory unit.
10. The method according to claim 9, wherein the card memory device is a card memory device according to any of the claims 1 to 8.